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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10050745	FILING DATE 01/16/2002	CLASS 324, 1	SUBCLASS 151, 111	GAU 2888	EXAMINER <i>T. K. Kim</i>
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\*\*CONTINUING DATA VERIFIED:

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\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-007673 01/16/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		4041P-000033
Verified and Acknowledged Examiners's initials		
TITLE : Integrate circuit device		

U.S. DEPT. OF COMM /PAT. & TM-PTO-435L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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